	Panipat Institute of Engineering & Technology (Department of CSE-AI&ML)			
	Subject:Computer Architecture Subject code: PC-CS-AIML-309A 5th Sem			
	LESSON PLAN			
SNo	Topic	CO Covered	Assignment No.	Teaching Methodology
1	Unit 1: Introduction to Computer System	CO1	Assignment No.1	Board
2	Computer Organization and Architecture, Von neumann Architecture, Evolution and Computer generations	CO1	Assignment No.1	Board
3	Fixed point and floating point number representation	CO1	Assignment No.1	Board
4	Digital arithmetic algorithm for addition .	CO1	Assignment No.1	Video
5	Booth 's Algorithm for substraction.	CO1	Assignment No.1	PPT
6	Booth 's Algorithm for multiplication	CO1	Assignment No.1	Board
7	Booth 's Algorithm for division restoring method.	CO1	Assignment No.1	Board

8	Booth 's Algorithm for division Non restoring method.	CO1	Assignment No.1	Board
9	Memory Hierarchy,Main Memory and Auxillary memory	CO1	Assignment No.1	Board
10	Unit 2: Intruction codes and stored programmorganization,computer register	CO2	Assignment No.2	Board
11	Commom bus system,computer instruction and timing control	CO2	Assignment No.2	Board
12	Instruction cycle,register reference instrutrcution,memory reference instructions	CO2	Assignment No.2	Board
13	Input ,outputandinterrupt:configuraton ,Instructions ,program interrupt	CO2	Assignment No.2	PPT & Board

14	Interrupt cycle,microprogrammed control organization ,control memory,addresss equecncing	CO2	Assignment No.2	Board
15	Microinstrctionformat ,horizontal vs vertical micro programming	CO2	Assignment No.2	Board
16	Revision of Unit-2	CO2	Assignment No.2	Flip Learning
17	Unit-3 Generl register organization,	CO2	Assignment No.2	Board
18	Stack Organization	CO2	Assignment No.2	Board
19	Instruction Formats	CO2	Assignment No.2	Board
20	Addressing modes	CO2	Assignment No.2	Flip Learning
21	Addressing modes	CO3	Assignment No.-3	Board
22	Data transfer and manipulation	CO3	Assignment No.-3	Board
23	Programm control	CO3	Assignment No.-3	Board
24	CISC and RISC, features and comparison	CO3	Assignment No.-3	Video
25	Pipelne and vector processing	CO3	Assignment No.-3	Board
26	Parallel processing	CO3	Assignment No.-3	Board

27	Flynn's taxonomy	CO3	Assignment No.-3	Board
28	Pipelining instruction pipeline	CO3	Assignment No.-3	Board
29	Basics of vector processing	CO3	Assignment No.-3	Board
30	Array processor	CO3	Assignment No.-3	PPT
31	Numerical on addressing modes	CO3	Assignment No.-3	PPT
32	Revision of Unit-3	CO3	Assignment No.-3	Flip Learning
33	Unit 4:I/O interface,I/O Bus and interface module	CO3	Assignment No.-3	PPTS
34	I/O versus memory bus	CO4	Assignment No.4	PPTS
35	Asynchronous data transfer :strobe control	CO4	Assignment No.4	PPTS
36	Handshaking, Asynchronous serial transfer	CO4	Assignment No.4	PPTS
37	Modes of transfer,Programmed I/O,Interrupt driven I/O ,Priority interrupt	CO4	Assignment No.4	PPTS
38	Daisy chaining,parallel priority interrupt	CO4	Assignment No.4	PPTS

39	DMA,Input/output processor,serial communication	CO4	Assignment No.4	PPTS