





Department of ECE

Presents

Volume 7, Edition 3 (July - Sept 2022)

# About Department of ECE

### Vision

To excel globally in technical education and research in the field of electronics and communication engineering and thus contribute to the welfare of society.

### Mission

M1: To establish a unique learning environment to enable the students to face the ever-emerging challenges in electronics and communication engineering.

M2: To equip the students with a broad intellectual spectrum and prepare them for diverse and competitive career paths.

M3: To provide practical orientated education and foster tie-up with national/International educational institutes, research bodies, and industry to support students and faculty development pursuits.

M4: To provide ethical and value-based education by promoting activities addressing societal needs.

### Program Educational Objectives (PEOs)

**PEO1:** Be able to successfully practice electronics and communication engineering with acquired skills and knowledge.

**PEO2:** Be receptive to new technologies and attain professional competence through advanced education, research work, and other professional activities.

**PEO3:** To prepare graduates who will practice their profession with ethics, integrity, and social responsibility in a global context.

**PEO4:** To develop leadership qualities with demonstrable attributes and to contribute to societal needs.



# From Director's Desk

It gives me immense satisfaction that next issue of ECE Newsletter is ready for the readers. A college Newsletter mirrors the success story of an institution and act as a great medium to reach out to the outer world. It reflects upon the persistent and committed efforts made by faculty, staff and students for taking the institution one step ahead. Continuing the same tradition, this issue of Nexus reflects upon commendable contribution made by all members of PIET family in their fields of expertise as well as for the overall growth of the college.

I congratulate everyone for their bit of service for the institution and do expect the same in times to come. I also congratulate the editorial team for bringing out present issue of newsletter.

Wish you good luck!

Prof. (Dr.) Shakti Kumar (Director)



### From HOD's Desk

IDear Readers
Greetings to you!!

The newsletter is a forerunner of all departmental technical activities. With the well qualified faculty & energetic students, the club aims and continuously works for the technical enhancement. The newsletter covers the activities & achievements of the students & faculty. I am pleased to present the issue before the readers.

Please feel free to drop in your suggestions to : monika.ece@piet.co.in

Dr. Monika Gambhir (HOD, ECE)



## Editor's Note

It is a matter of pride as well as pleasure to present before our readers next issue of Department Newsletter. We feel honored for the faith reposed in us for per- forming the role of editors of Department Newsletter. We have put whole-hearted endeavors to give a complete and kaleidoscopic view of laudable achievements of ECE department. Through further issues of Nexus, we do hope that we will come up to the expectations of our readers.

Please feel free to drop in your suggestions to: sapna.ece@piet.co.in

Sapna Arora Assistant Prof. ECE





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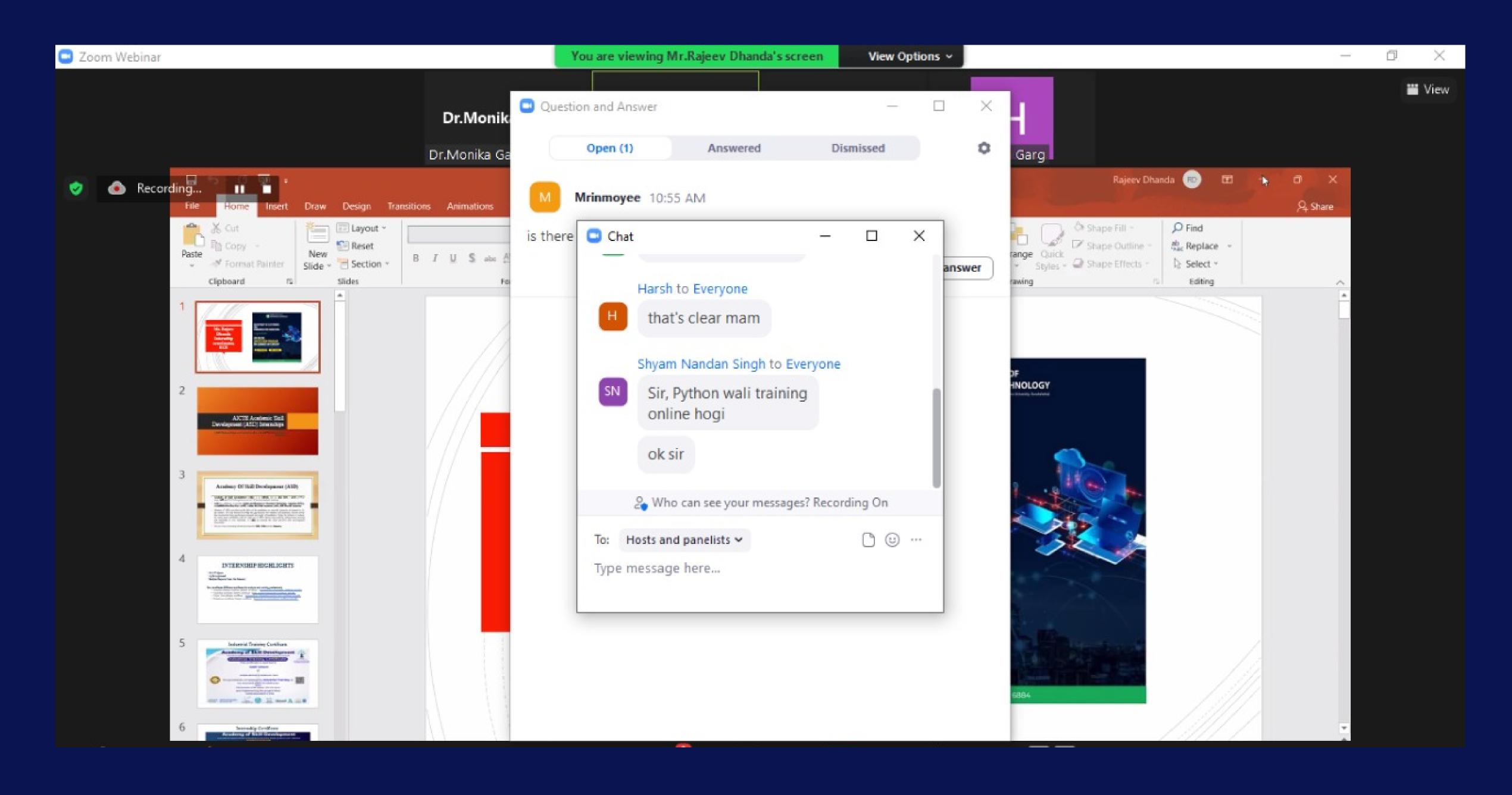
# Orientation session on summer internship

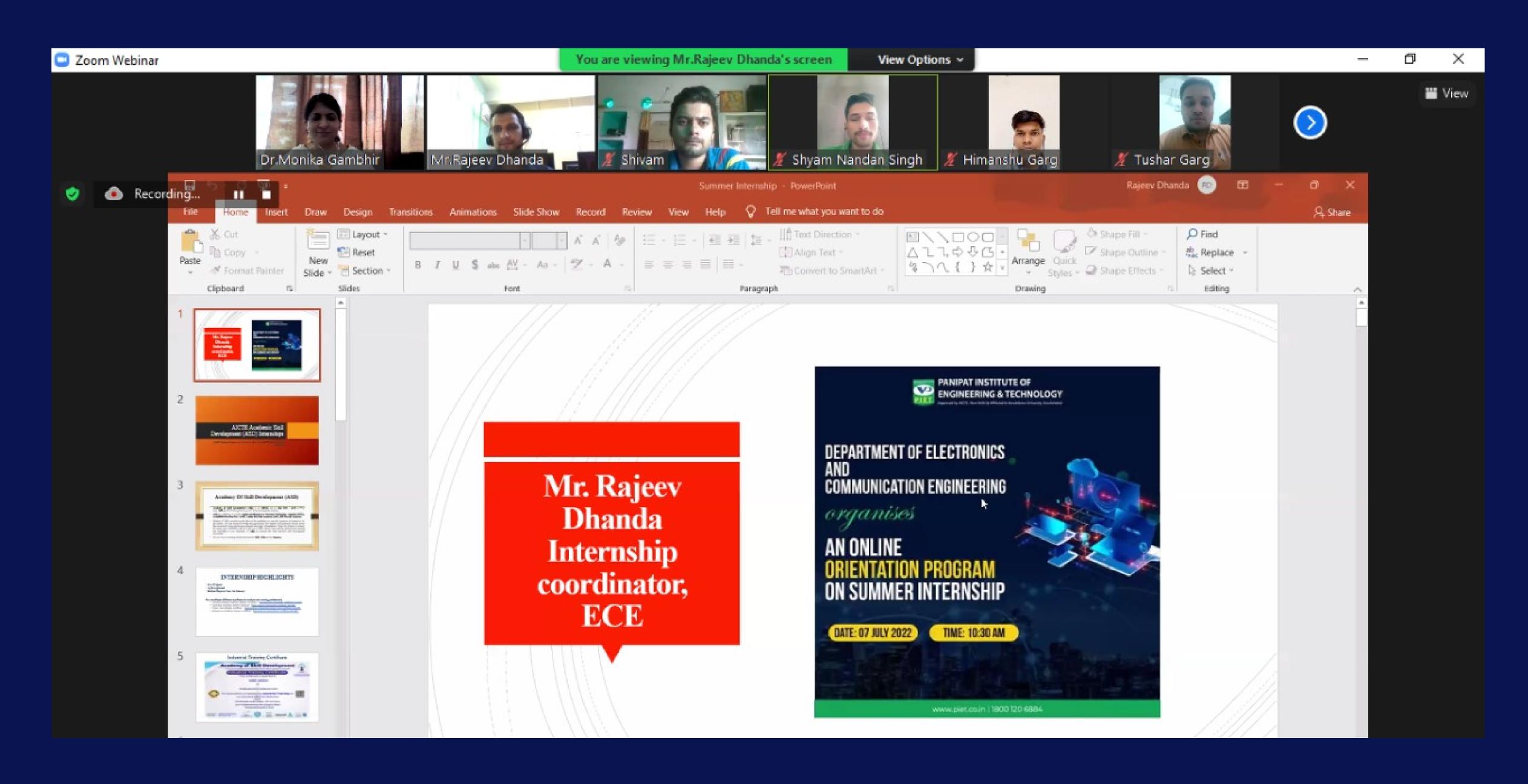
The Department of ECE organized an online orientation session on July 7, 2022 on zoom, Mr. Rajeev Dhanda, Assistant Professor, ECE delivered this session with the primary objective of creating awareness among students about potential areas for summer internships. The session also aimed to share information about the AICTE Academic Skill Development Initiative and the Sandeepani School of VLSI Design & Embedded System, which provides job-oriented trainings.



# Glipmses from the event







# FDP on Digital System Design

The Electronics and Communication Department in collaboration with AICTE IDEA Lab organized "FDP on Digital System Design using Xilinx Vivado Tool & Implementation on PYNQ FPGAs" from July 18-23, 2022. This Short-Term Training Program (STTP) aimed to provide participants with an opportunity to enhance their knowledge and skills in the field of Digital System Design. The program covered fundamental concepts of HDL programming, design of combinational and sequential circuits, FPGA design flow, and productivity using Python on the PYNQ Z2 boards.

#### Objectives of the FDP

This faculty development program aims at training the faculty to design & develop digital systems using Xilinx Vivado tools. The FDP focuses on hardware modeling, verification & synthesis using Verilog and its implementation on PYNQ Z2 FPGA boards.

#### **Registration Form:**

Name of Participant:

Designation:

Department:

**Institute:** 

Area of Specialization:

Date:

Place:

Sessions Overview:

- Introduction to HDLs, CPLDs & FPGAs.
- Xilinx Vivado Design Flow.
- Hardware modeling & verification of combinational & Sequential circuits in Verilog.
- Synthesis & Implementation of RTL codes on PYNQ Z2 boards.

#### Expected Outcomes:

#### Participants will be able to

- Write VHDL & Verilog code for combinational and sequential circuits.
- Simulate behavioral HDL models for combinational and sequential circuits.
- Implement the design on Xilinx PYNQ Z2 FPGA boards

Co-ordinators & Contact Details

Dr. Poonam Jaglan Associate Professor ECE

Signature

Mr. Rajesh Kuamr Assistant Professor ECE





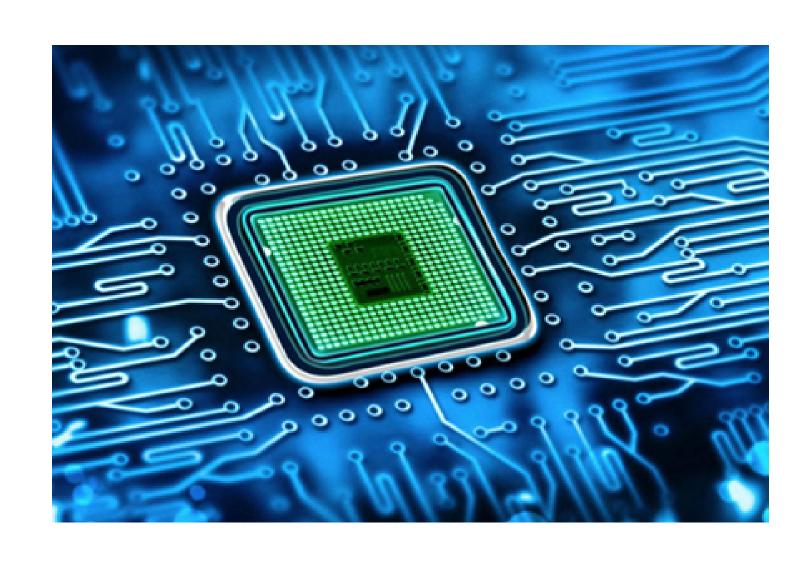
AICTE IDEA lab Sponsored

Faculty Development Program

011

Digital Systems Design with Xilinx Vivado & Implementation on PYNQ Z2 board

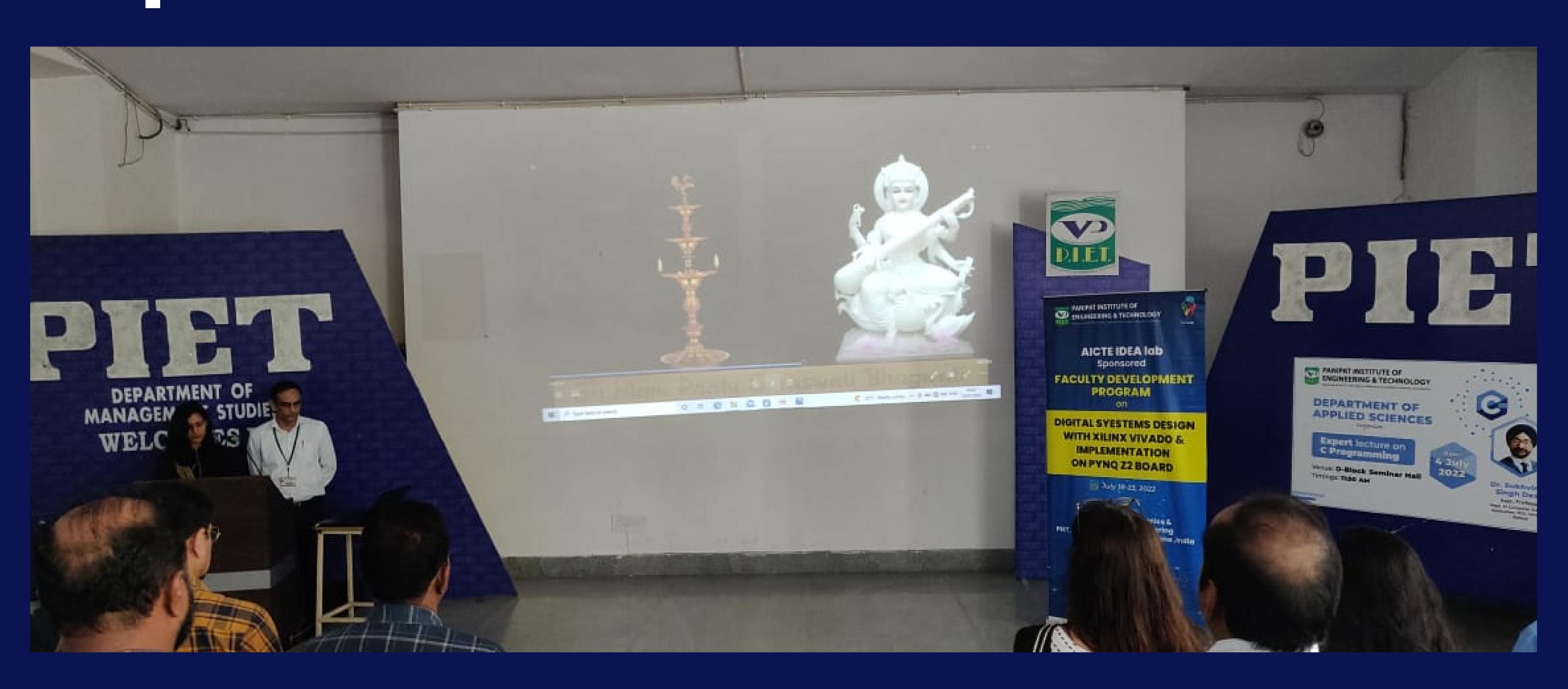
July 18-23/2022



Organized by
Department of Electronics & Communication
Engineering

PIET, Samlakha, Panipat, Haryana, India

# Glipmses from the event







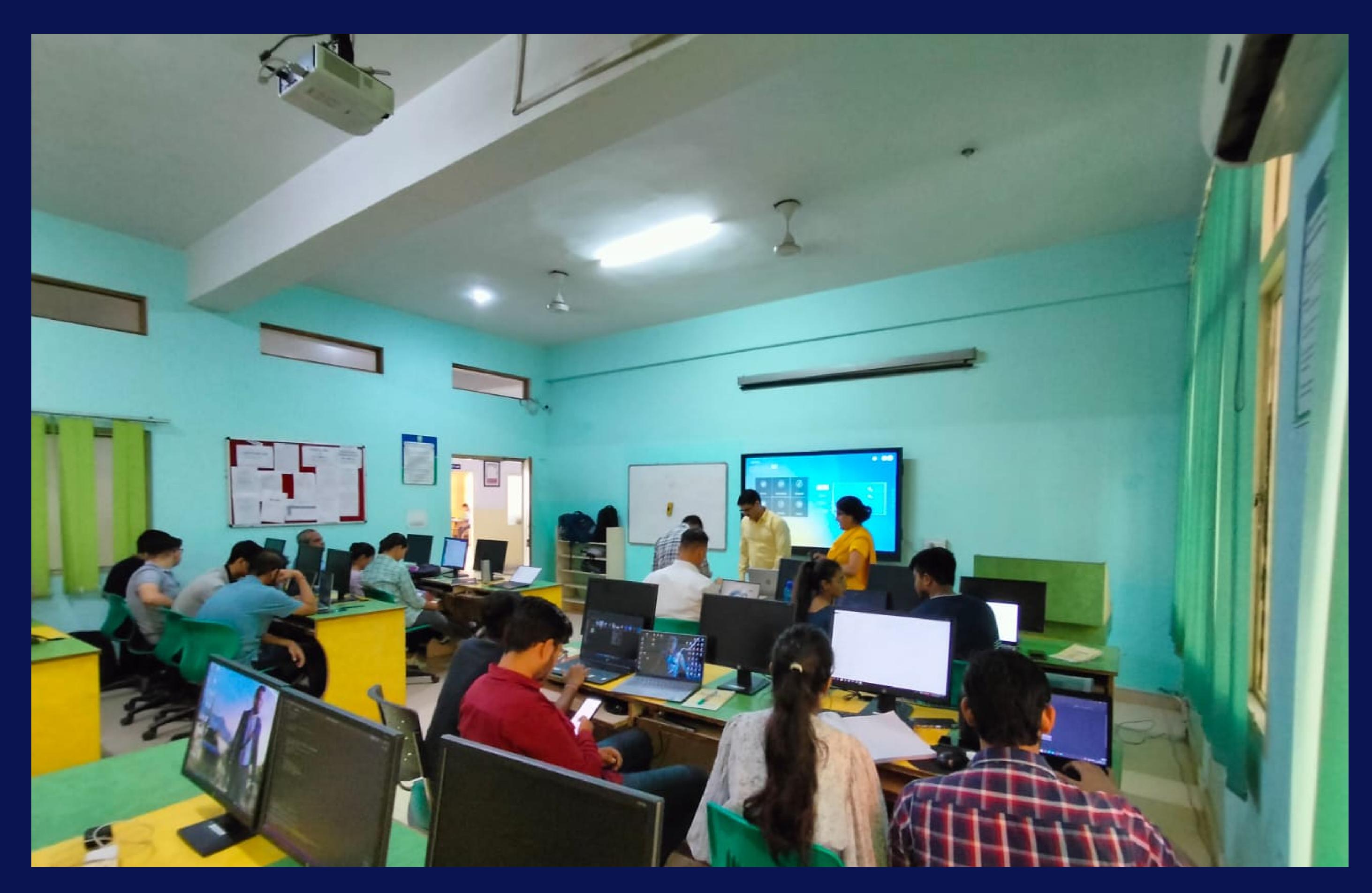
# Advanced Course on Embedded System and Internet of Things

The ECE department organised a Value Addition Program (VAP) on "Advanced Course on Embedded System and IoT" scheduled from **September 5th to 9th, 2022**. This project-based learning initiative aims to enrich students by integrating software and hardware concepts in the field of Embedded Systems and IoT. **Himanshu Tripathi**, Scientific Officer, I/C Fab LAB AKGEC, AKGEC Skills Foundation facilitated the session. The Value Addition Program on Advanced Embedded System and IoT promised to be an enriching experience for participating students, offering them valuable insights and practical skills in cutting-edge technologies.



# Glipmses from the event





## Talent Hunt

Talent Hunt, an event aimed at providing a platform for students to showcase their talents across various fields, was organized on **September 28, 2022**, by **Ms. Sapna Arora and Mr. Yeeshu**, Assistant Professors in the Department of Electronics and Communication Engineering (ECE). The event not only provided an opportunity for participating students to demonstrate their abilities but also served as a source of inspiration for others to unveil their hidden potential.

Panipat Institute of Engg. and Technology
Department of Electronics and Communications
Yantra Club Organising

TOUTYOUR TALENT September 28, 2022 E-Block Seminar Hall Ground floor

Reward Points to winners and participants will be given as per department policy

REGISTER YOURSELF AT LINK GIVEN BELOW
THIS POSTER

You can showcase any talent you have. like Dance, Singing, Poetry, Mimicry, Comedy

Gunjan (ECE 3rd Yr) Prerna (ECE 2nd Yr)

8950841040 7015700562

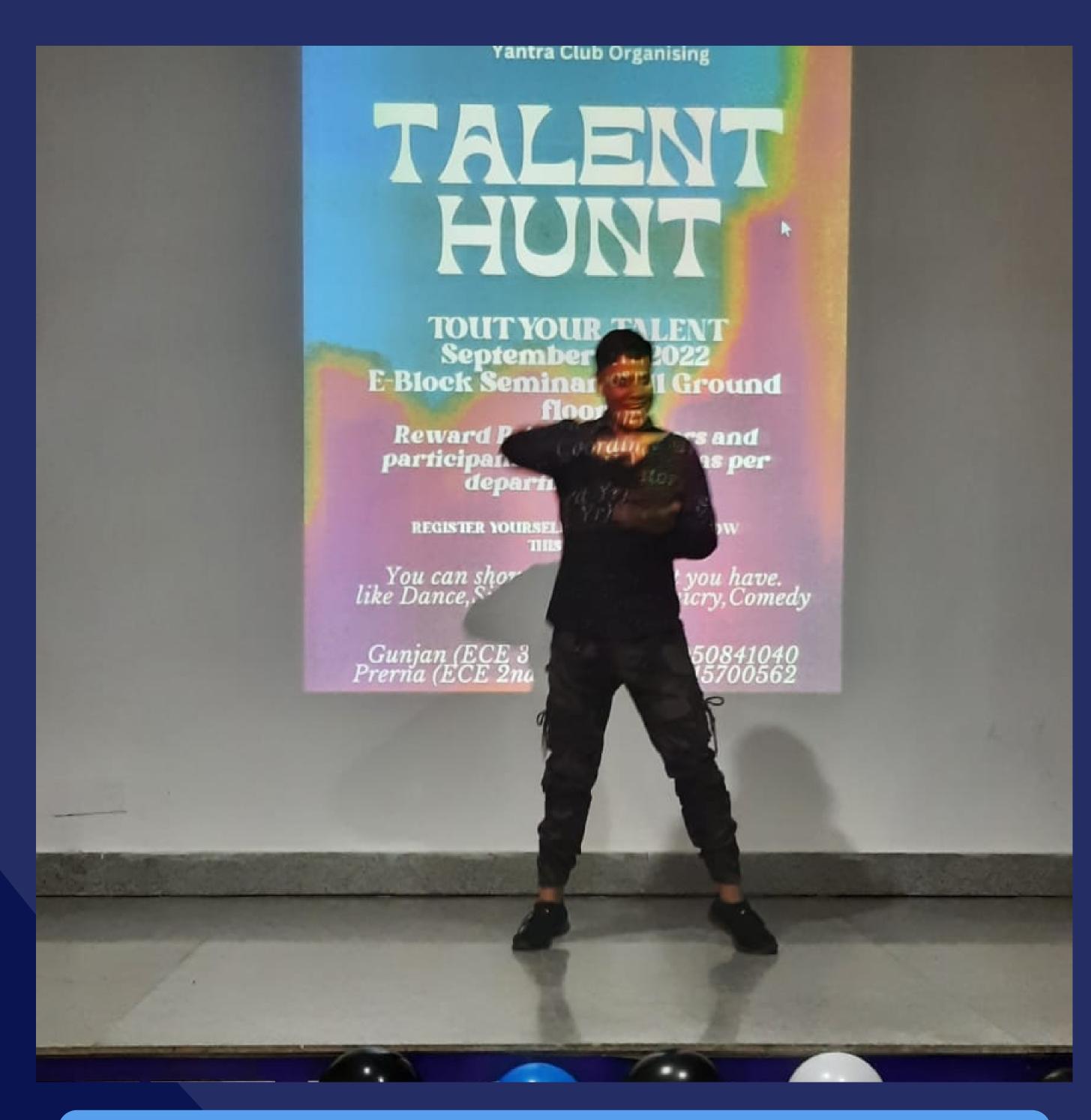
# Glimpses from the event:



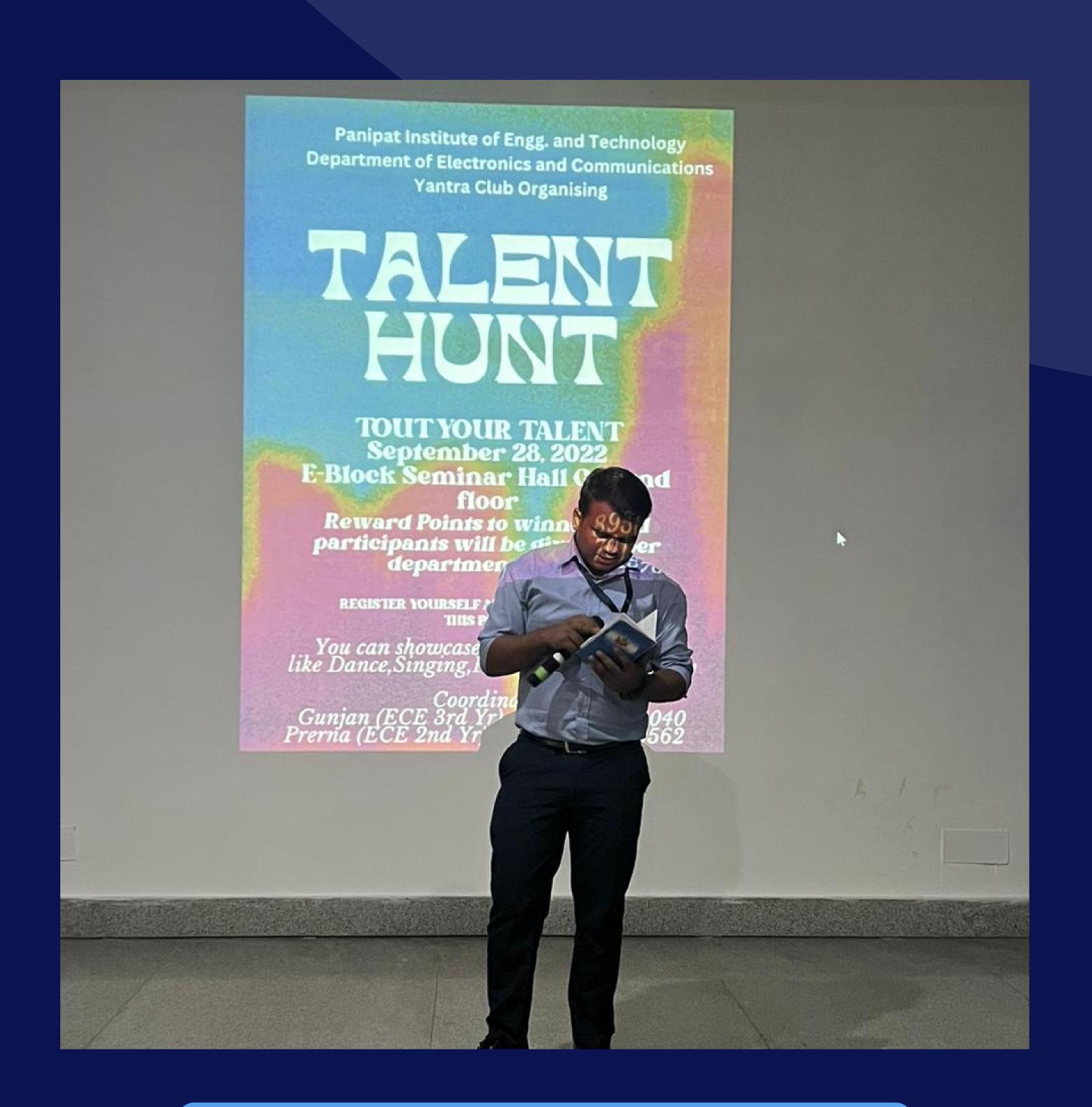
Neeraj Jha, 3rd yr singing a song



Riya and Purva performing dance



Shivam Tyagi, 1st yr presenting a dance



Roshan, Final yr recited a poem



Gunjan 3rd yr and Deepak 2nd yr anchored the event

At the end of the event, winners were announced which included the following participants:

#### Singing

Neeraj Jha	1st Position
Aditya	2nd Position
Surbhi	3rd Position

#### Dance

Shivam	1st Position
Riya	2nd Position

### Photography

Srishti	1st Position
Nitin	2nd Position
Sanjay	3rd Position

### Poetry

Roshan	1st Position
Sourav	2nd Position
Lucky	3rd Position