PANIPAT INSTITUTE OF ENGINEERING AND TECHNOLOGY PANIPAT **DEPARTMENT OF CYBER SECURITY LESSON PLAN**

Subject:Software Engineering Semester: 3rd Subject code: PC-CS-CYS-207A

501	nester:		
No. Lec No		Topics to Be covered	Mode of
			Teaching
1	L 1	Software & Software Engineering: Introduction & Characteristics	White Board
2	L 2	Software crisis: Factors & Remedies	Smart Board
3	L 3	Software engineering paradigms : Classical Waterfall Model	White Board
4	L 4	Software engineering paradigms: Iterative Waterfall Model	White Board
5	L 5	Prototype Model	Smart Board
6	L 6	Software engineering paradigms : Spiral Model	Smart Board
7	L 7	Planning a software project	Smart Board
8	L 8	Software cost estimation	White Board
9	L 9	Project scheduling	White Board
	L 10	Personal planning, Team structure	White Board
	L 11	Software configuration management : Introduction	Smart Board
	L 12	Project Monitoring	White Board
	L 13	Risk Management	White Board
	L 14	Software requirement analysis- Classification, Structure Analysis	Smart Board
	L 15	Object Oriented Analysis, Data Modeling	Smart Board
	L 16	Software requirement Specification &validation	Smart Board
#	L 17	Design and implementation of software: Software design	White Board
#		fundamentals	
	L 18	Design methodology:Structured design	White Board
	L 19	Design methodology :Object Oriented design	White Board
	L 20	Design Verification, Monitoring and Control coding	Smart Board
	L 21	Software Reliabilities: Metric and specification	White Board
	L 22	Module Coupling and Cohesion	Smart Board
	L 23	LOC ,Cyclomatic Complexity	Smart Board
	L 24	COCOMO Model	Smart Board
	L 25	Software Quality Assurance	White Board
	L 26	Testing – Testing fundamentals, Test Cases	White Board
	L 27	White Box Testing Strategies	White Board
	L 28	White Box Testing Strategies	Smart Board
	L 29	Black Box Software Testing Strategies	White Board
	L 30	Black Box Software Testing Strategies	White Board

L 34	Maintenance: key Issues	Smart Board
L 35	Types Of Software maintenance	Smart Board
L 36	Cost Of Maintenance, Software Re-Engineeringcycle	Smart Board